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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/666,845	09/19/2003	Takashi Fujise	9319S-000532	5792
27572	7590 12/23/2004		EXAMINER	
HARNESS	, DICKEY & PIERCE,	NGUYEN, KHANH V		
P.O. BOX 828 BLOOMFIELD HILLS, MI 48303			ART UNIT	PAPER NUMBER
	,		2817	-
			DATE MAILED: 12/23/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)			
	10/666,845	FUJISE, TAKASHI			
Office Action Summary	Examiner	Art Unit			
	Khanh V. Nguyen	2817			
The MAILING DATE of this communication app Period for Reply	pears on the cover sheet with the	correspondence address			
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a repl - If NO period for reply is specified above, the maximum statutory period of Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be ti y within the statutory minimum of thirty (30) da will apply and will expire SIX (6) MONTHS fron e, cause the application to become ABANDONE	mely filed ys will be considered timely. In the mailing date of this communication. ED (35 U.S.C. § 133).			
Status					
1) Responsive to communication(s) filed on 19 S	eptember 2003.				
·—	s action is non-final.				
closed in accordance with the practice under E	Ex parte Quayle, 1935 C.D. 11, 4	53 O.G. 213.			
Disposition of Claims					
 4) Claim(s) 1-6 is/are pending in the application. 4a) Of the above claim(s) is/are withdra 5) Claim(s) is/are allowed. 6) Claim(s) 1-5 is/are rejected. 7) Claim(s) 6 is/are objected to. 8) Claim(s) are subject to restriction and/or 					
Application Papers					
9) The specification is objected to by the Examine	er.				
10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.					
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).					
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).					
11) ☐ The oath or declaration is objected to by the Ex	xaminer. Note the attached Office	e Action or form PTO-152.			
Priority under 35 U.S.C. § 119					
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the prio application from the International Burea * See the attached detailed Office action for a list	ts have been received. Is have been received in Applicat Inity documents have been receiv In (PCT Rule 17.2(a)).	tion No ed in this National Stage			
Attachment(s)					
1) X Notice of References Cited (PTO-892)	4) 🔲 Interview Summan	y (PTO-413)			
 Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date <u>9/19/03</u>. 	Paper No(s)/Mail D				

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DETAILED ACTION

Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 1 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Regarding claim 1, it seems that a first amplifier circuit and second amplifier circuit are mirror to one another. However, it is unclear why the first amplifier circuit supplies a current to an output terminal while the second amplifier circuit absorbs a current from the output terminal. As shown from drawing, it is not clear how the second amplifier circuit absorbs a current from the output terminal.

Regarding claim 2, it is not clear which circuit called "potential generating circuit".

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1-5 are rejected under 35 U.S.C. 102(e) as being anticipated by Itakura et al. (6,480,178).

Regarding claim 1, Itakura et al. (Fig. 31) disclose an amplifier circuit comprising: a first amplifier circuit (Mp41, Mp42) and a second amplifier circuit (Mn41, Mn42) receive first and second potential via (IN-, IN+); and a control circuit (POL) for selectively controlling the amplifier simultaneously.

Regarding claim 2, wherein inputs (IN+, IN-) can be read as a potential generating circuit and IN+ can be read as first potential and IN- can be read as a second potential.

Regarding claim 3, wherein the first amplifier circuit (Mp41, Mp42) and the second amplifier circuit (Mn41, Mn42) are differential amplifier having a common output (OUT).

Regarding claim 4, wherein switches (SW20, SW21) can be read as a first switches for the amplifiers which selectively cut off by control circuit (POL).

Regarding claim 5, wherein current sources (lb1, lb2) can be read as a second transistor for the amplifiers having the functions thereof.

Allowable Subject Matter

Claim 6 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claim 6 calls for, among others, the operation of the control circuit as claimed.

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Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. The additional references (Whatley (5,083,051); Xu (6,066,985); Fujise (6,501,252)) show further analogous prior art circuitry.

These arts are deemed relevant and should be carefully reviews before any amendment is filed.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Khanh V. Nguyen whose telephone number is (571) 272-1767. The examiner can normally be reached from 8:00 AM - 3:30 PM EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on (571) 272-1769. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9306 for regular communications and (703) 872-9306 for After Final communications.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

KHANH V. NGUYEN PRIMARY EXAMINER

Khauh and gurper